

Fig. 1

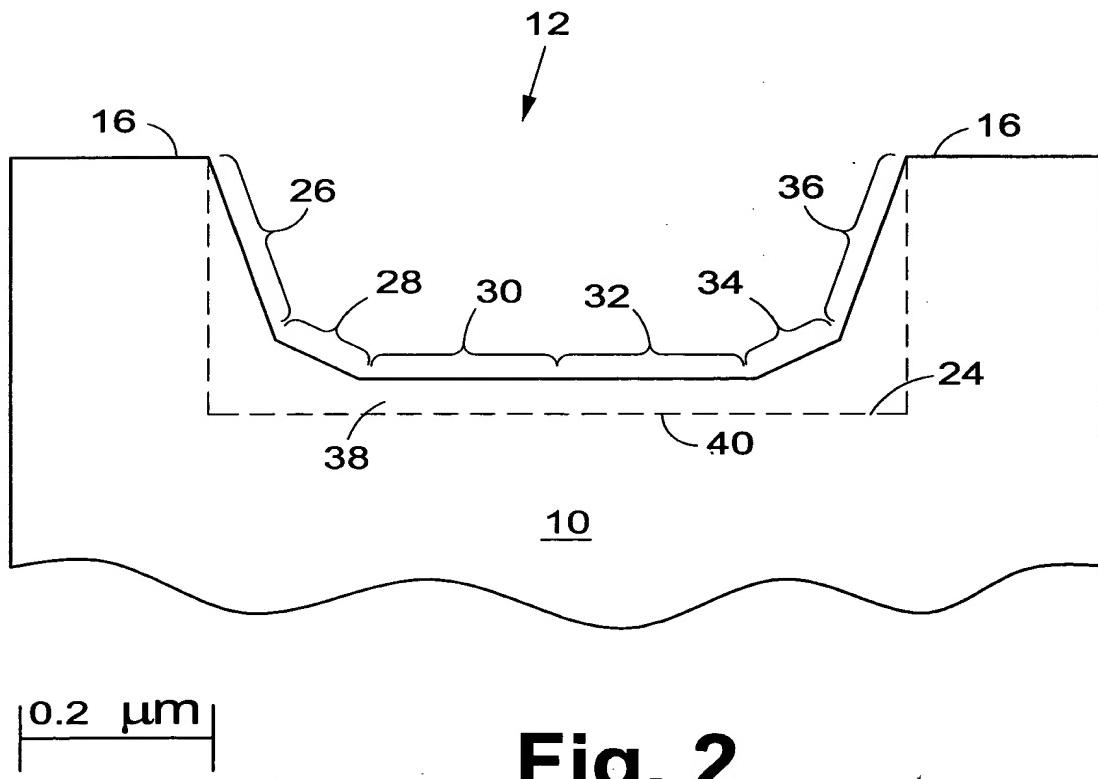


Fig. 2

VERTICAL TRANSISTOR AND METHOD OF MAKING
INVENTORS NAME: Fernando Gonzalez
DOCKET NO.: 303.776US1

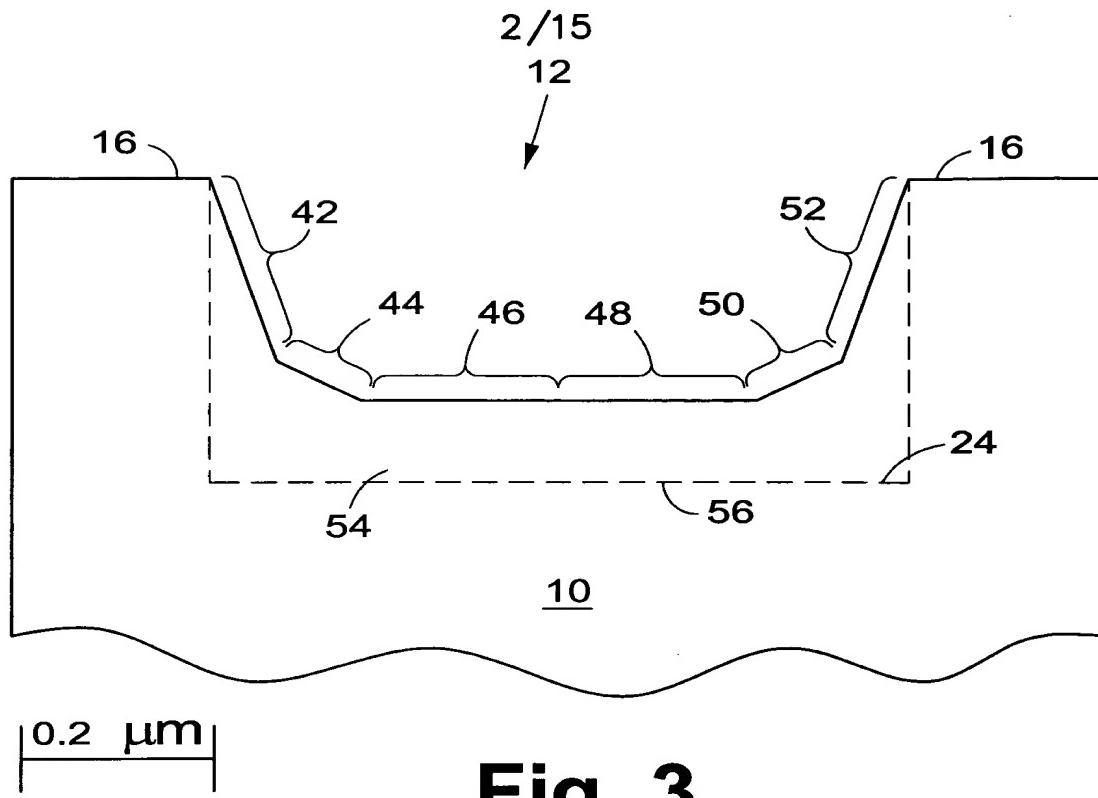


Fig. 3

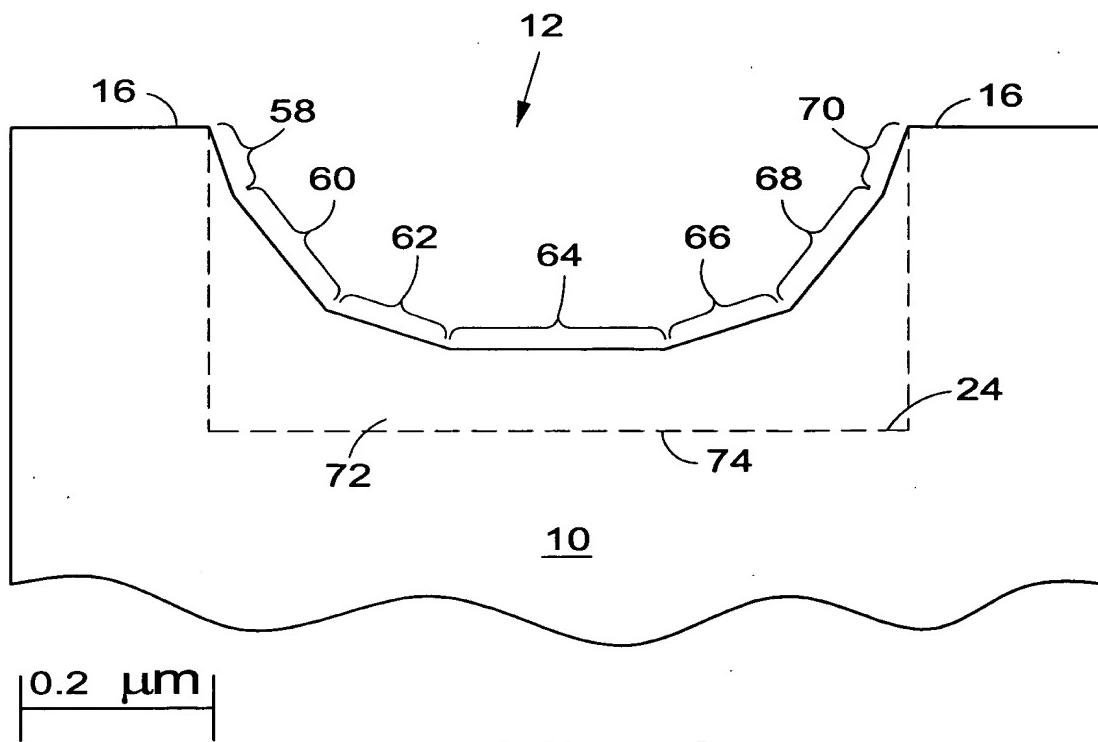


Fig. 4

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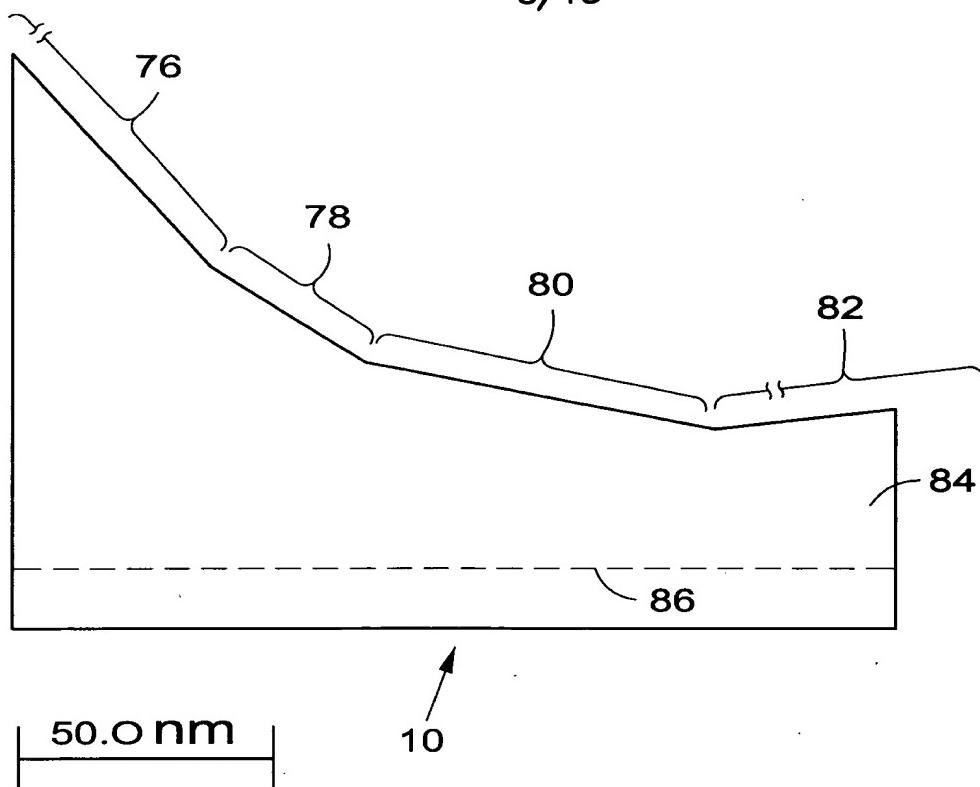


Fig. 5

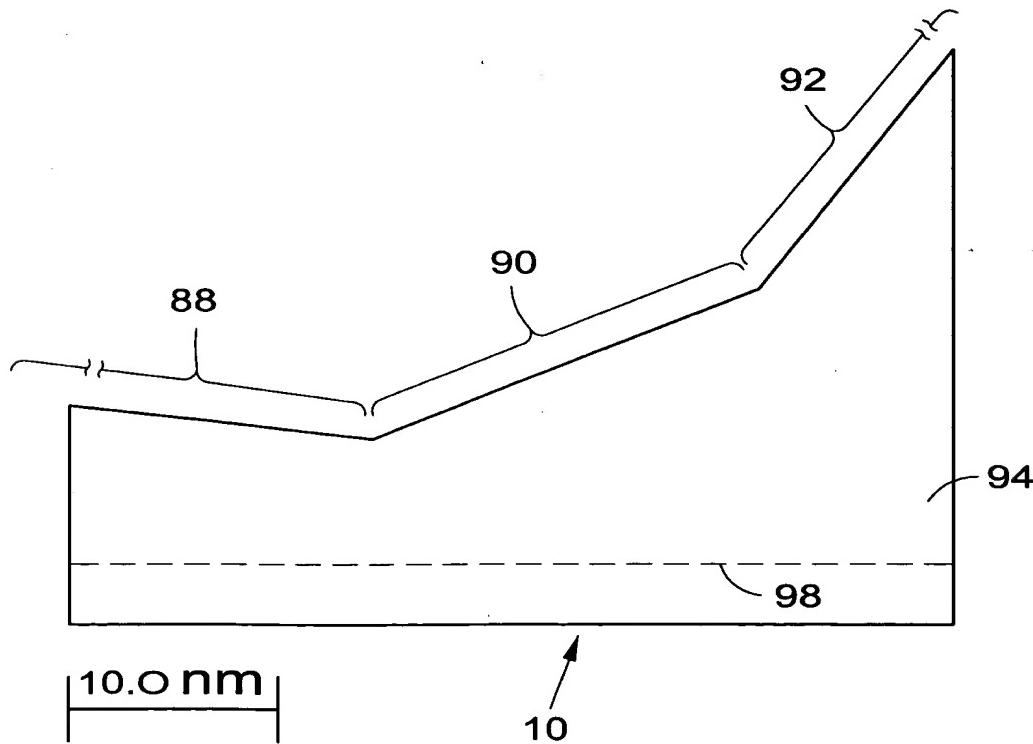


Fig. 6

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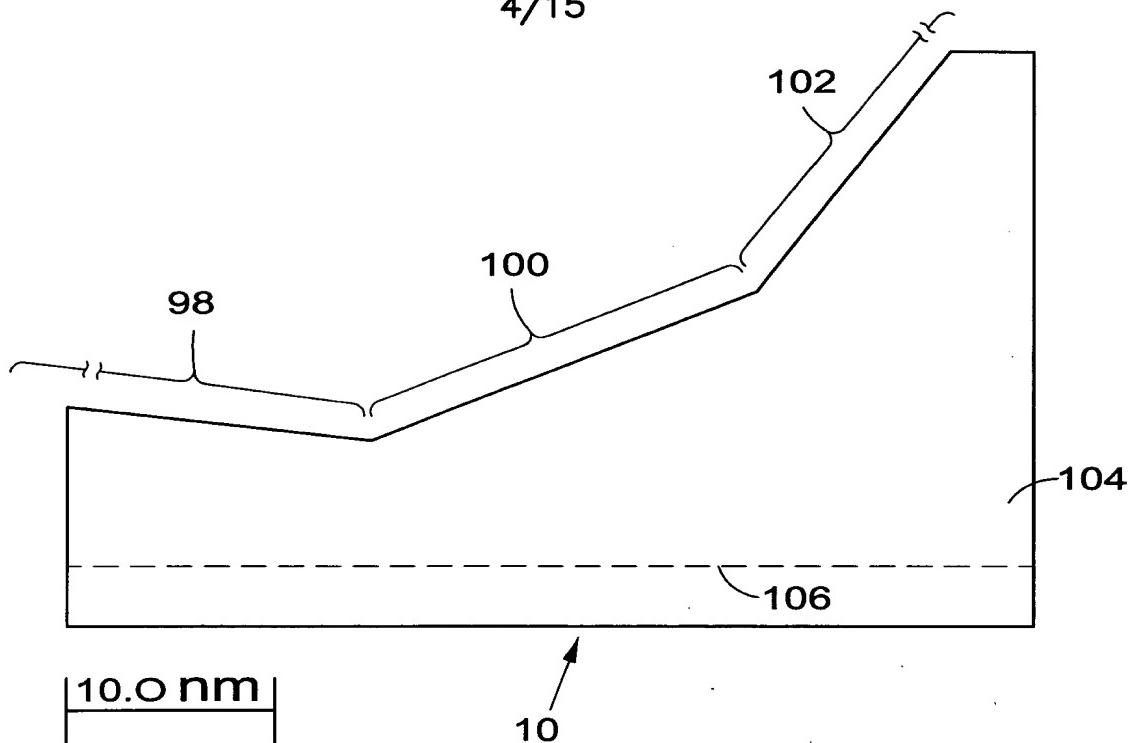


Fig. 7

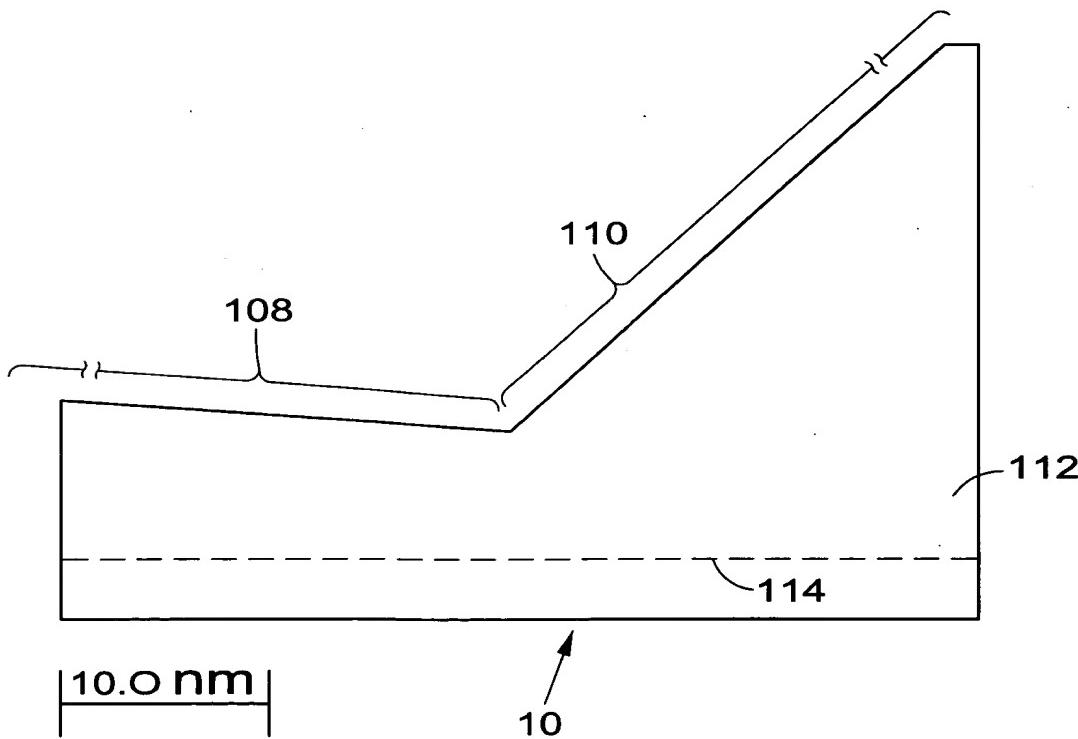


Fig. 8

VERTICAL TRANSISTOR AND METHOD OF MAKI
INVENTORS NAME: Fernando Gonzalez
DOCKET NO.: 303.776US1

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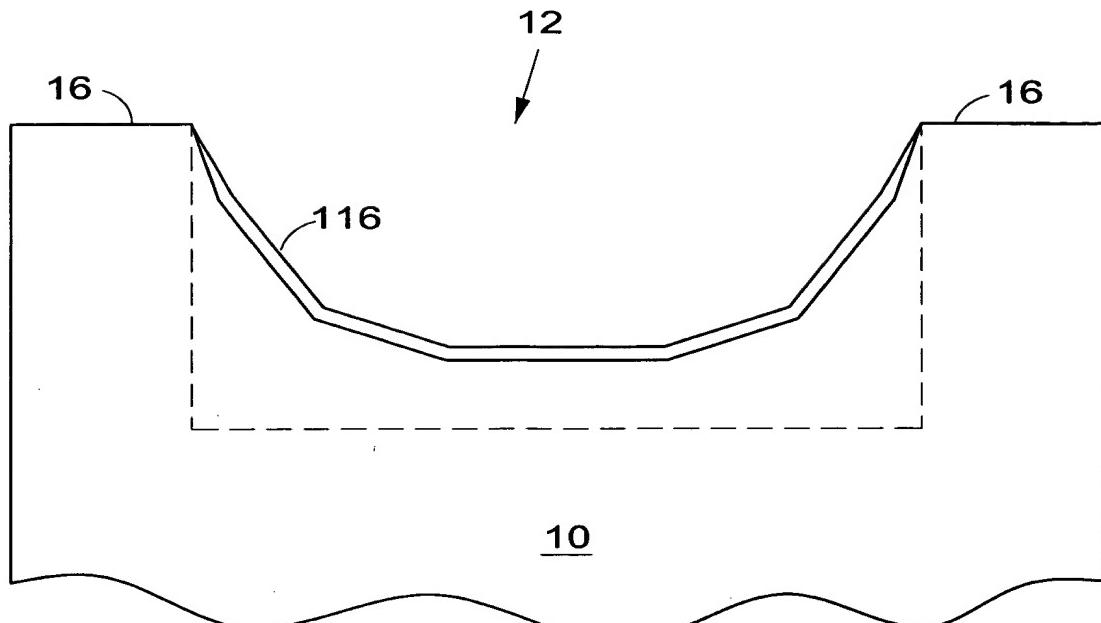


Fig. 9

VERTICAL TRANSISTOR AND METHOD OF MAKI
INVENTORS NAME: Fernando Gonzalez
DOCKET NO.: 303.776US1

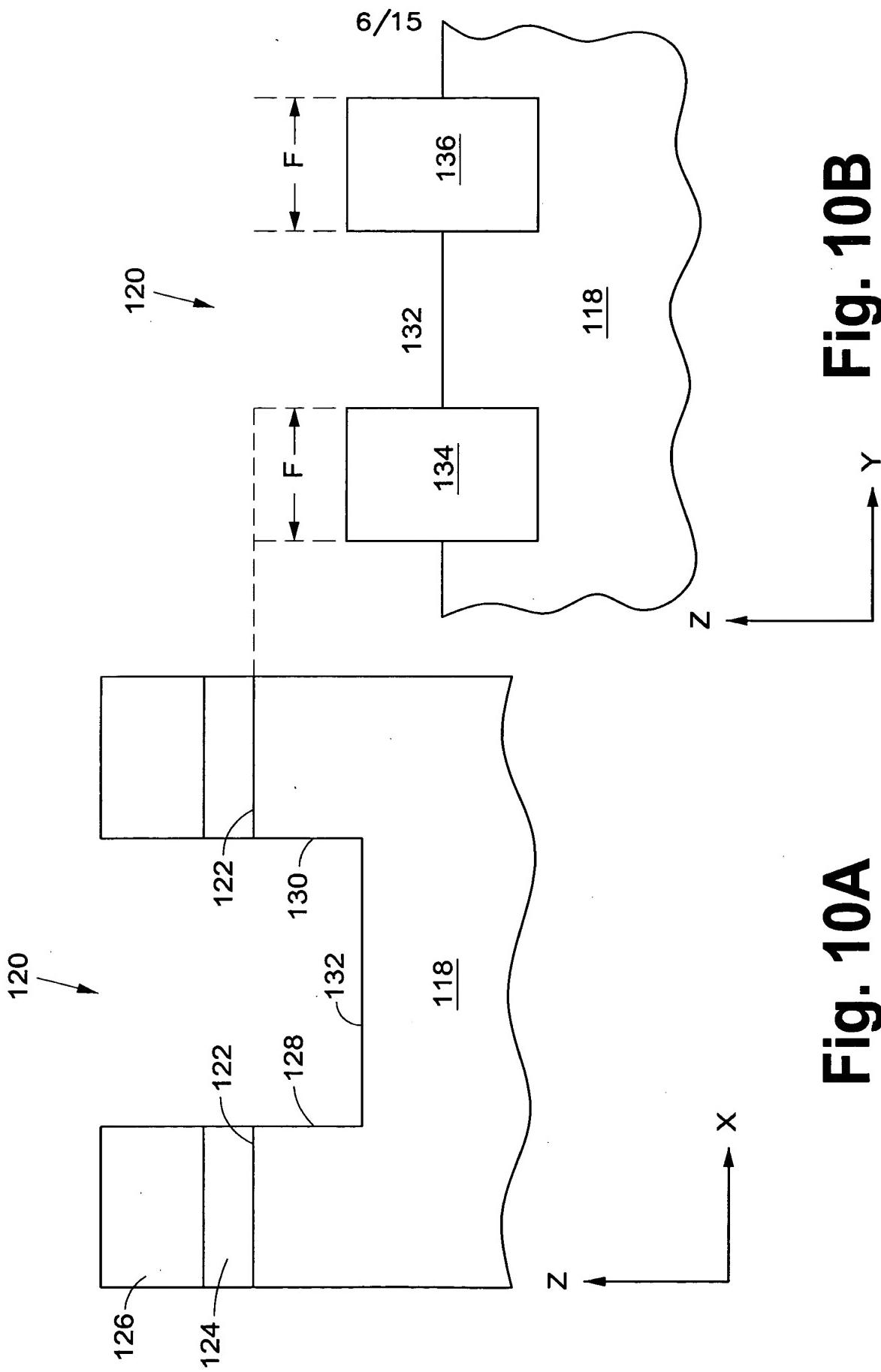


Fig. 10A

Fig. 10B

VERTICAL TRANSISTOR AND METHOD OF MAKI
INVENTORS NAME: Fernando Gonzalez
DOCKET NO.: 303.776US1

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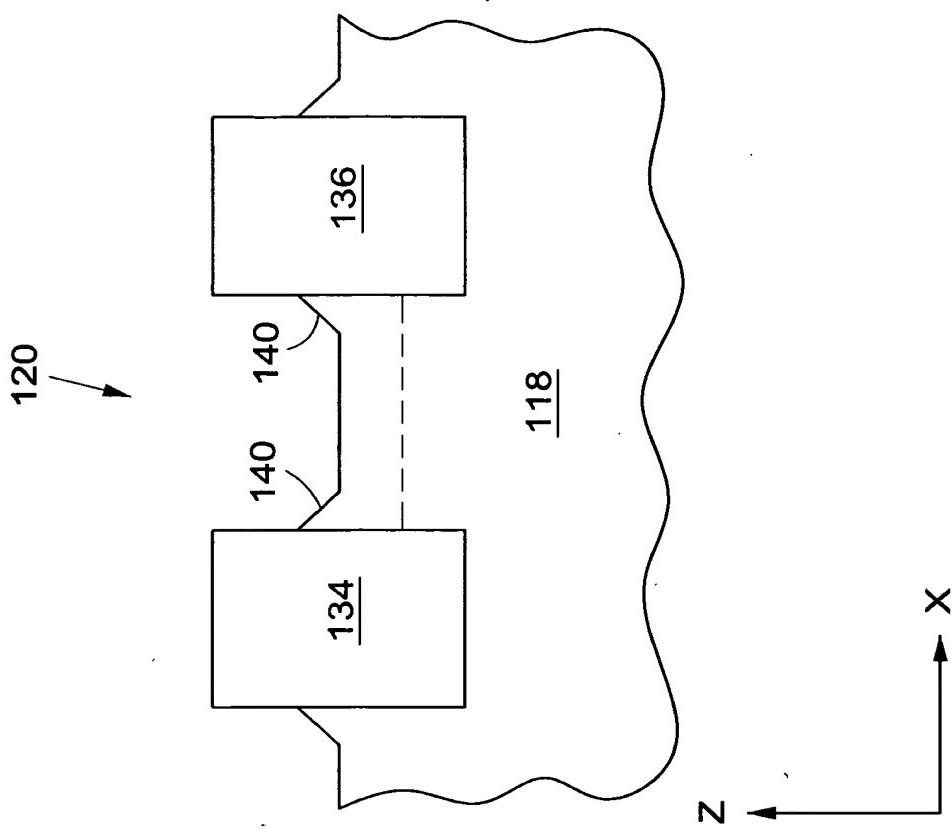


Fig. 11B

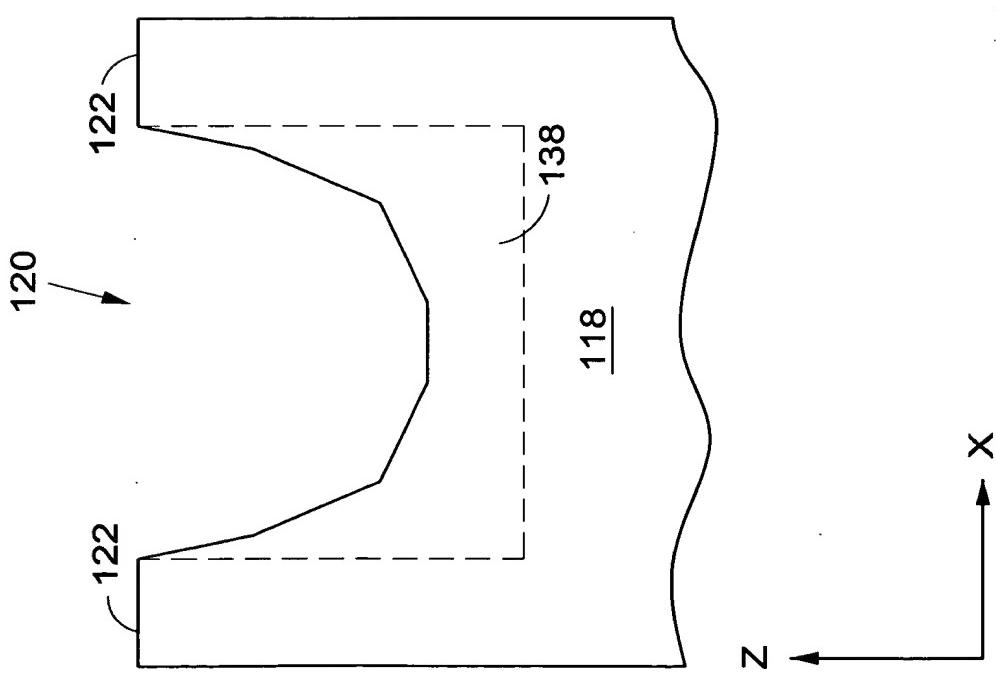


Fig. 11A

VERTICAL TRANSISTOR AND METHOD OF MAKING
INVENTORS NAME: Fernando Gonzalez
DOCKET NO.: 303.776US1

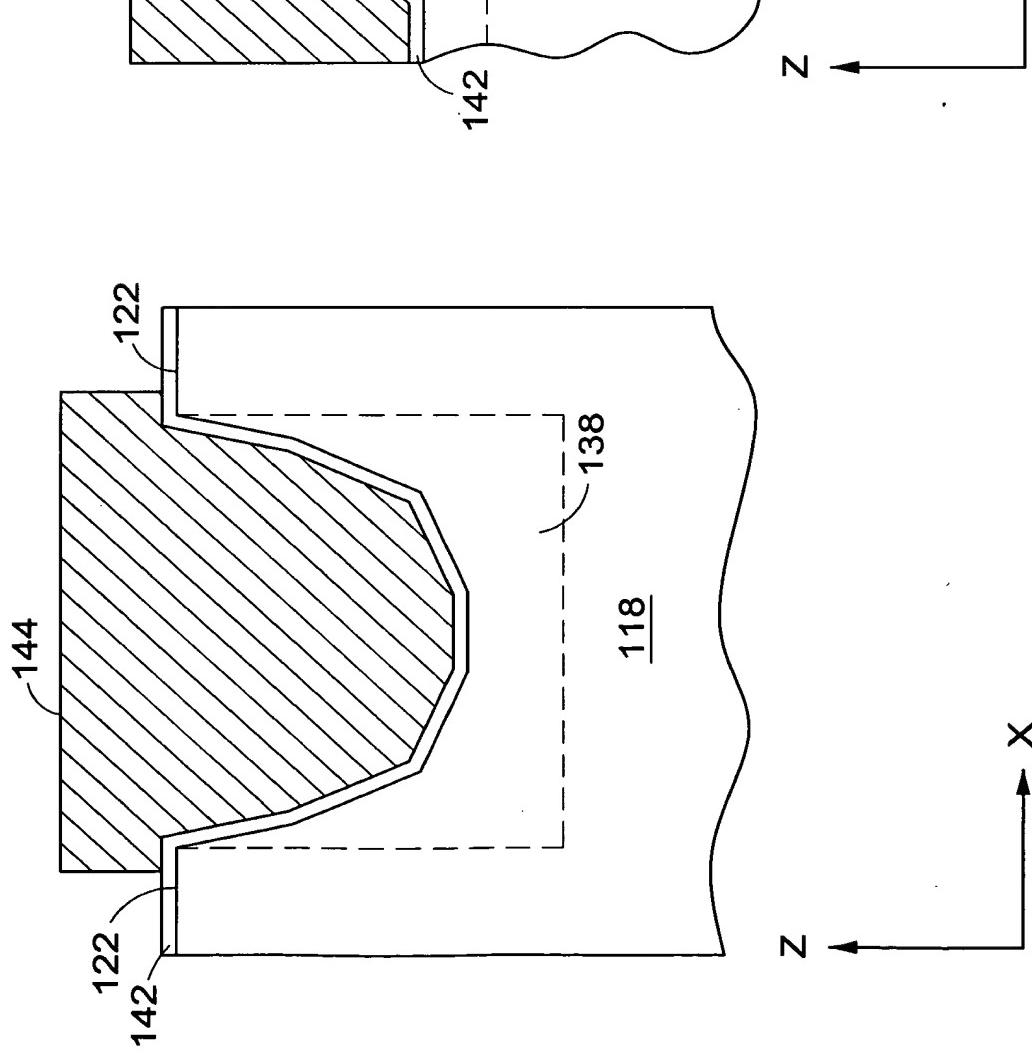
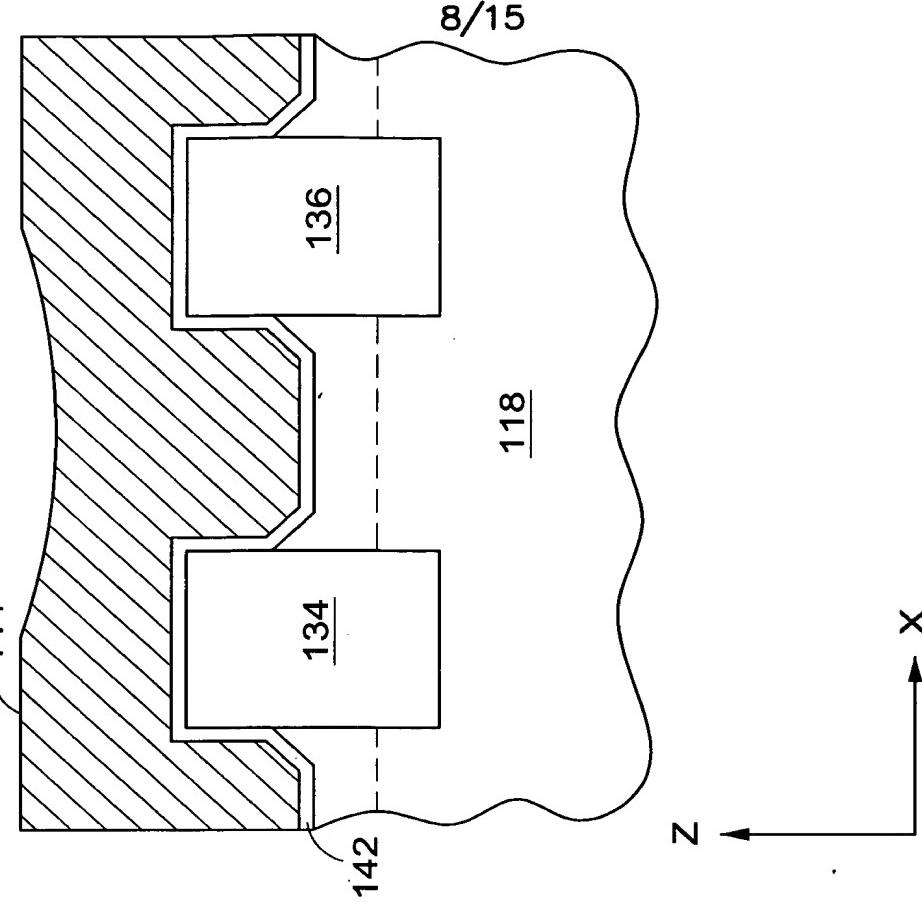


Fig. 12A

Fig. 12B



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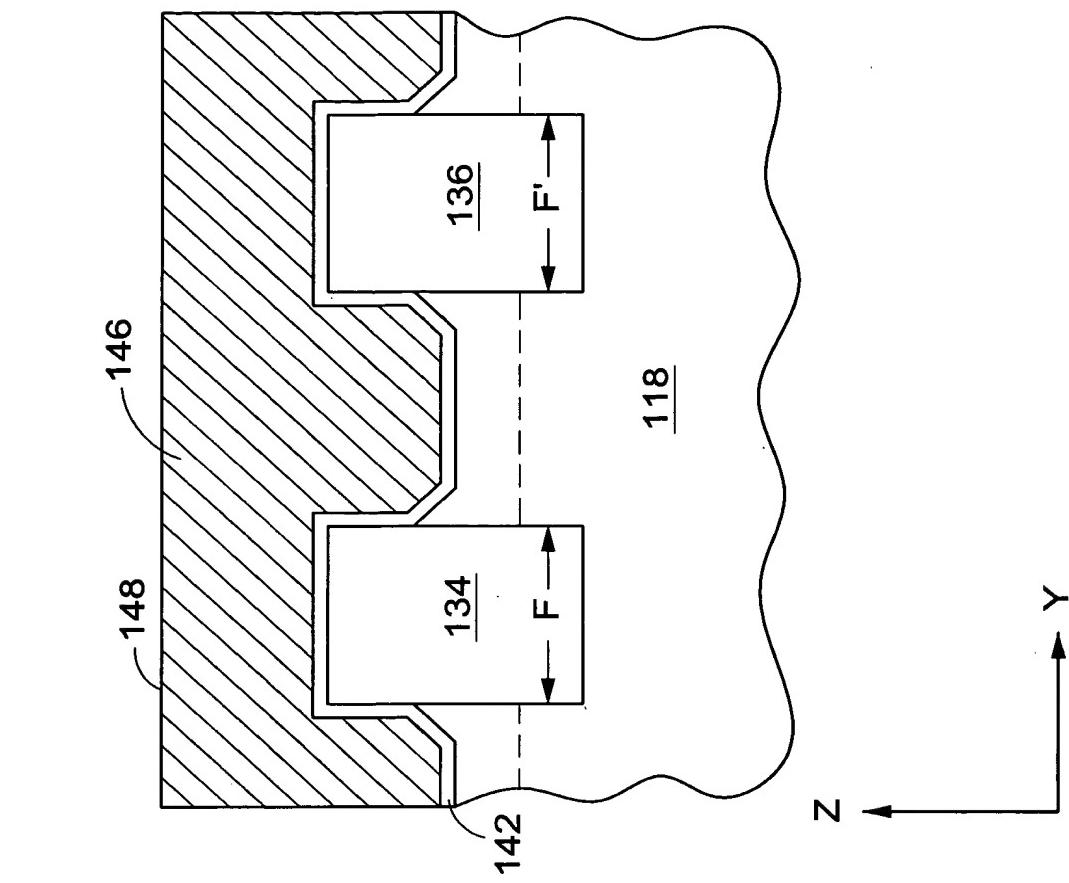


Fig. 13A

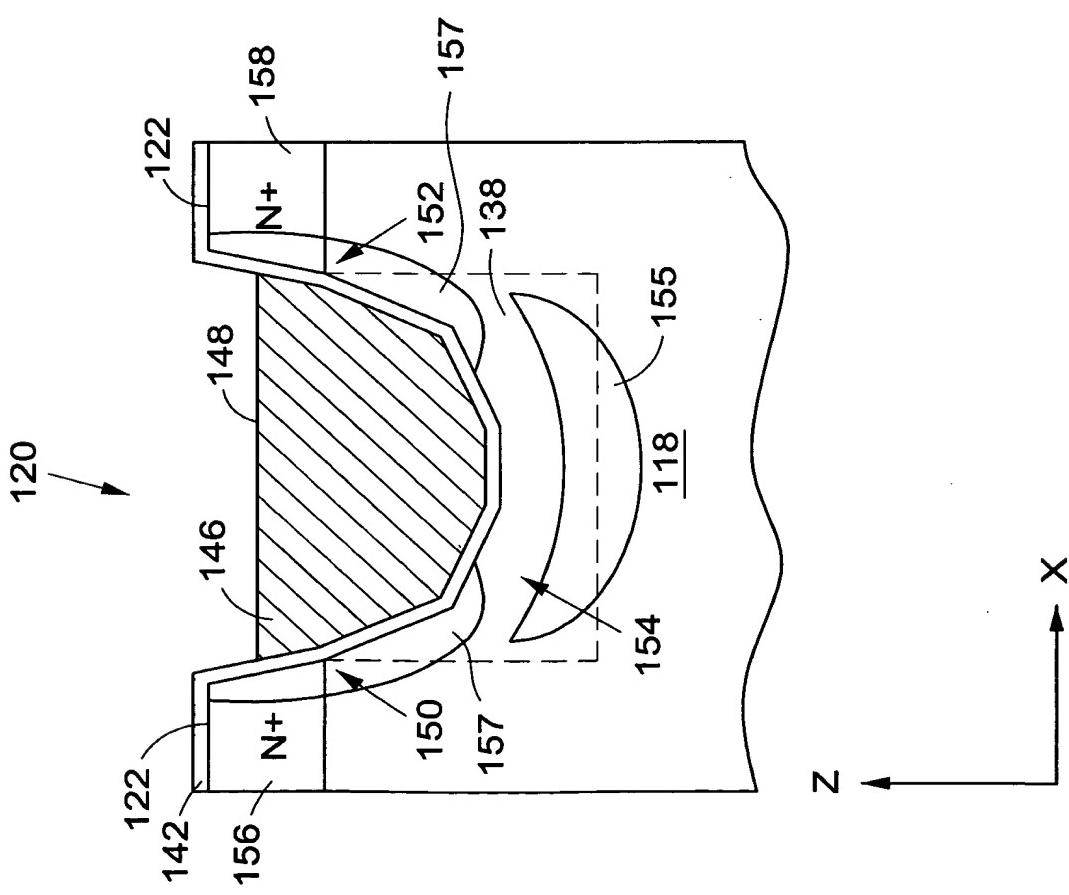


Fig. 13B

VERTICAL TRANSISTOR AND METHOD OF MAKING
INVENTORS NAME: Fernando Gonzalez
DOCKET NO.: 303.776US1

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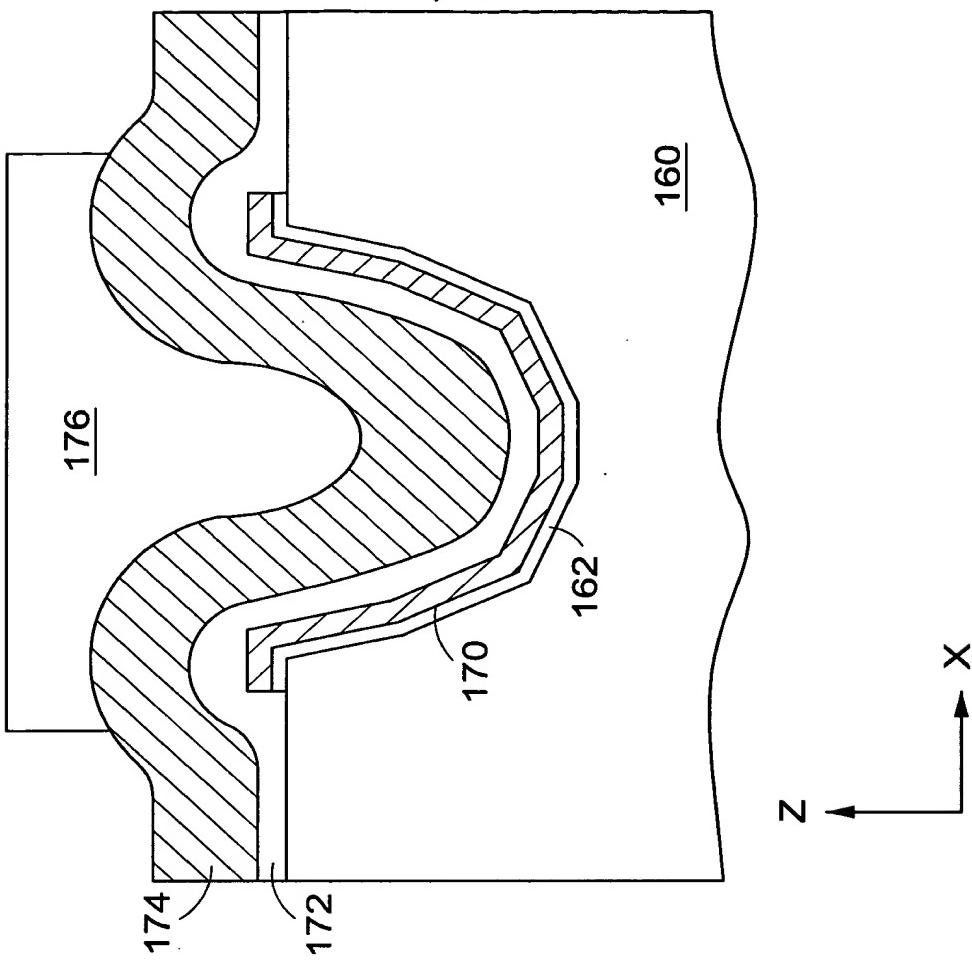


Fig. 15

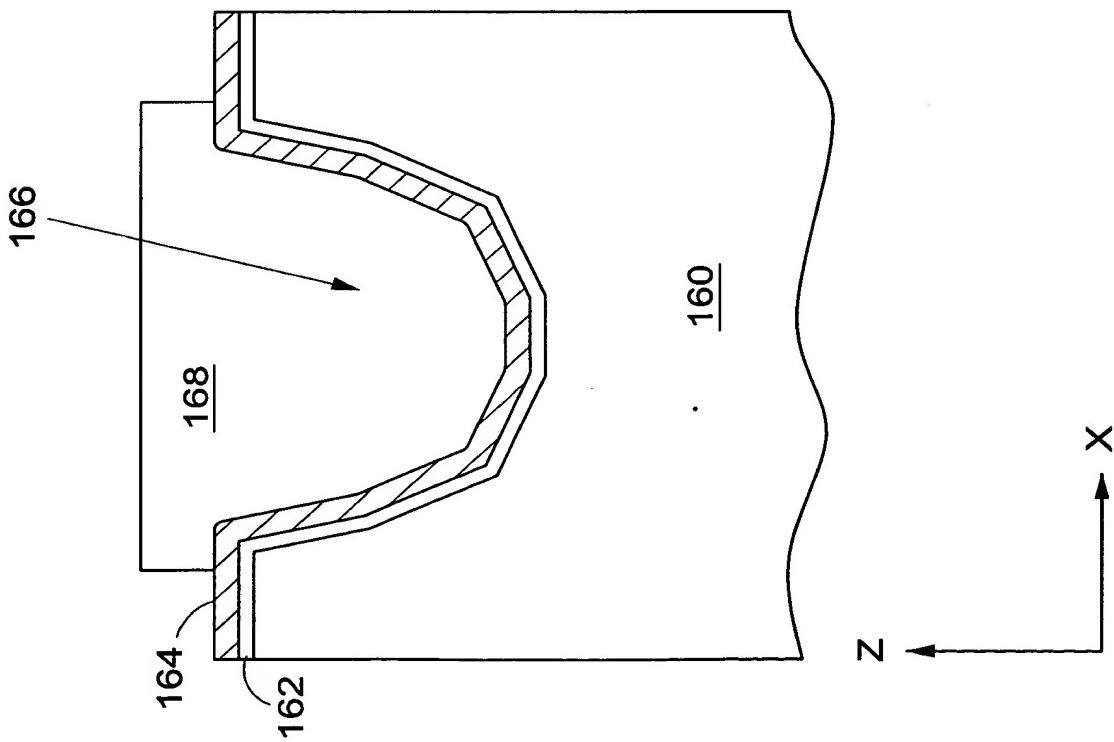


Fig. 14

Fig. 17

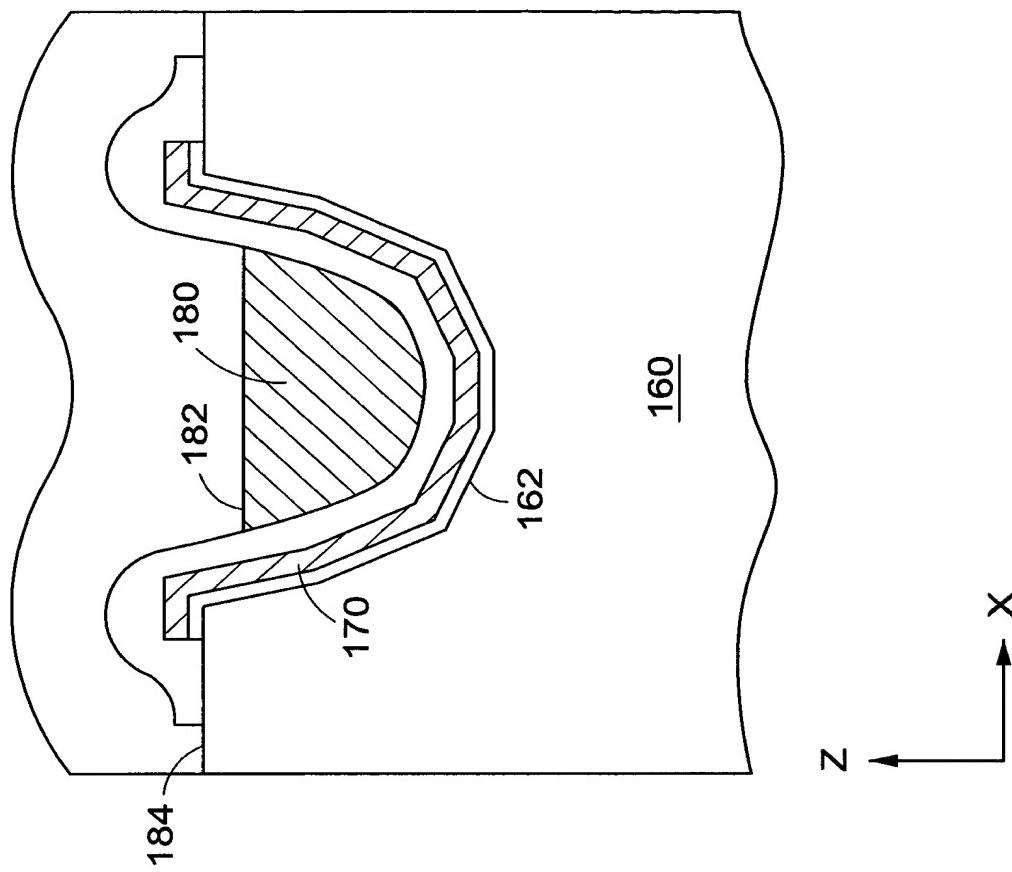
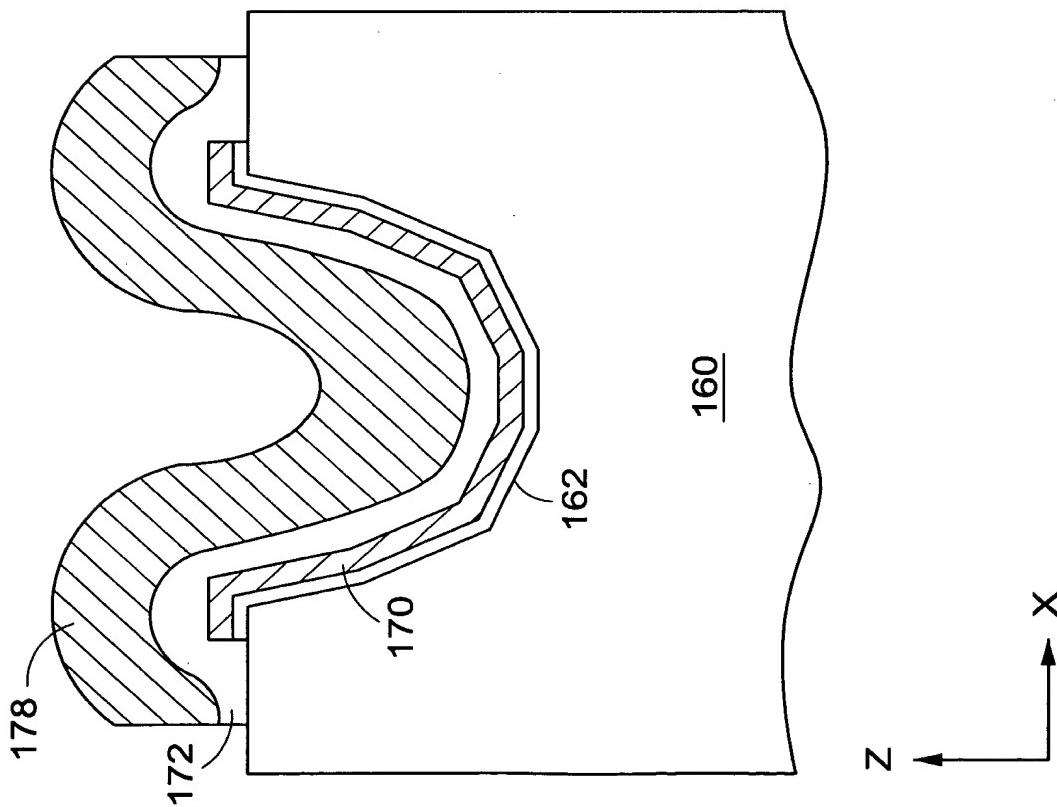


Fig. 16



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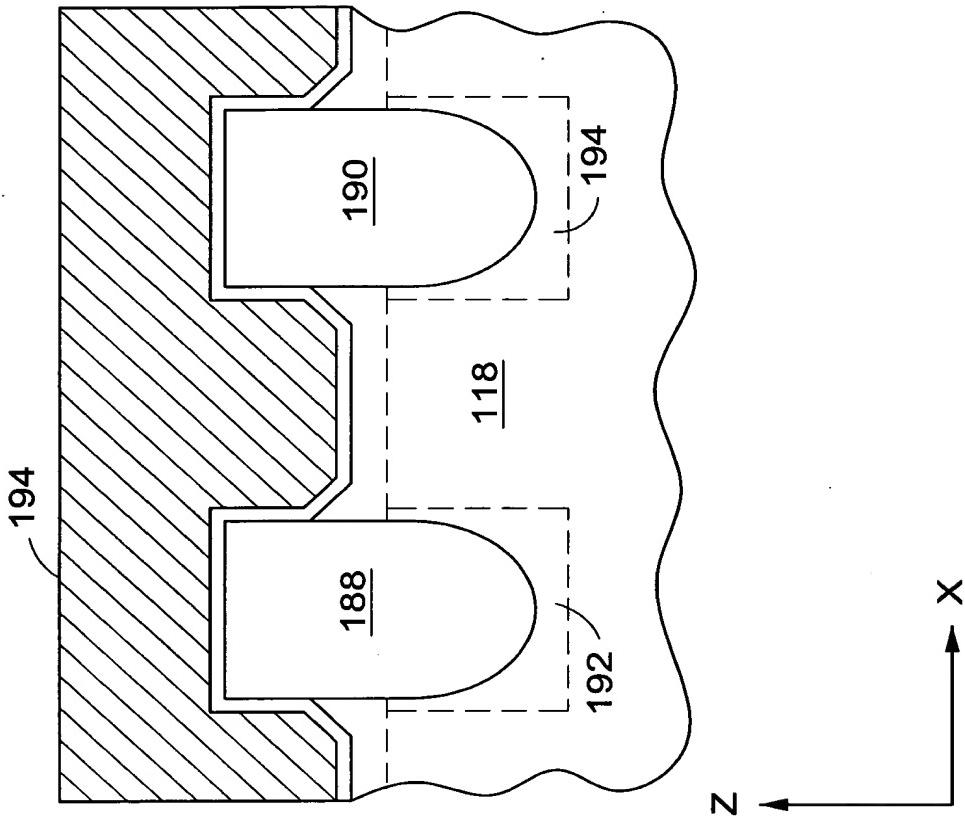


Fig. 18B

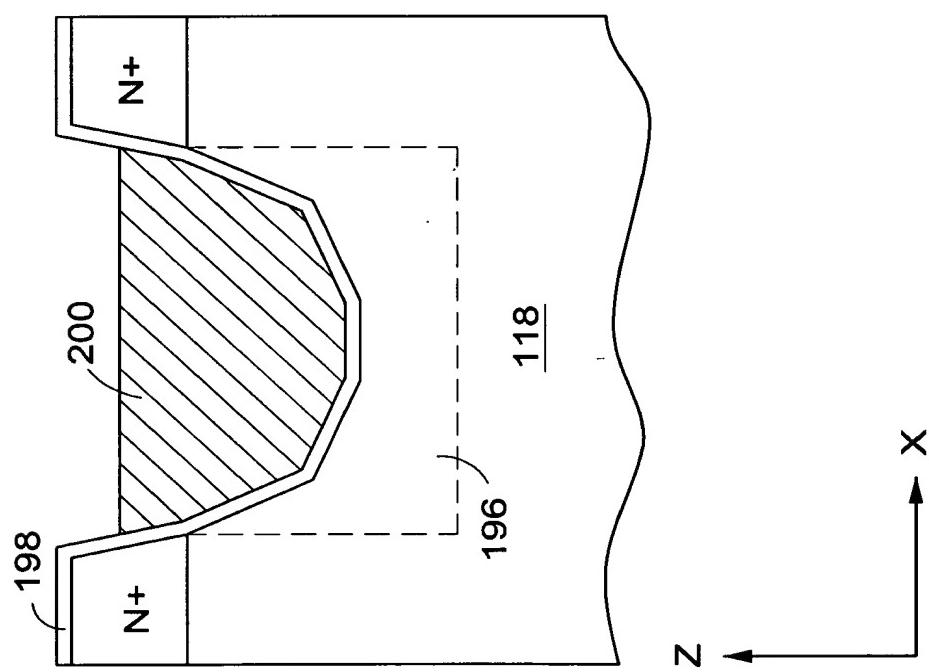


Fig. 18A

VERTICAL TRANSISTOR AND METHOD OF MAKING
INVENTORS NAME: Fernando Gonzalez
DOCKET NO.: 303.776US1

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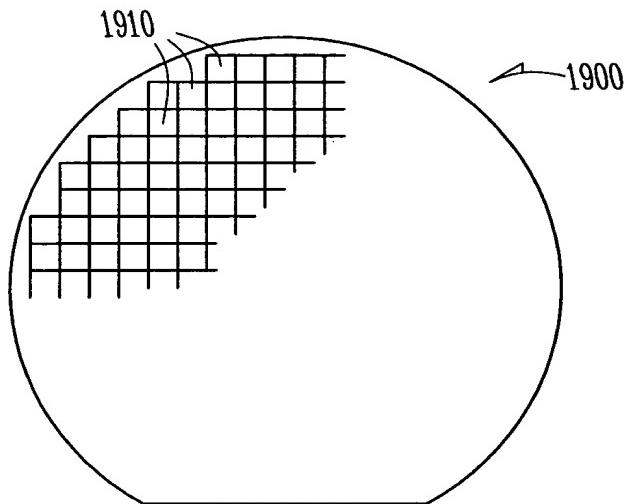


Fig. 19

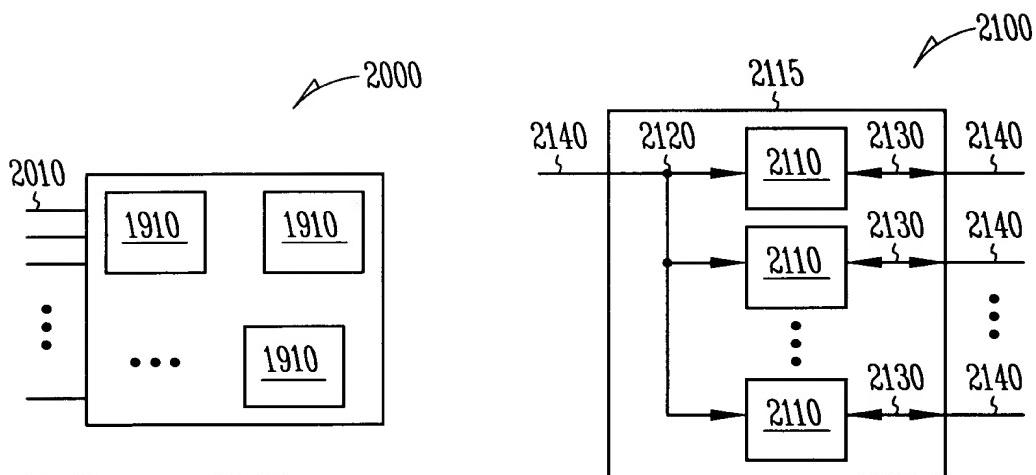


Fig. 20

Fig. 21

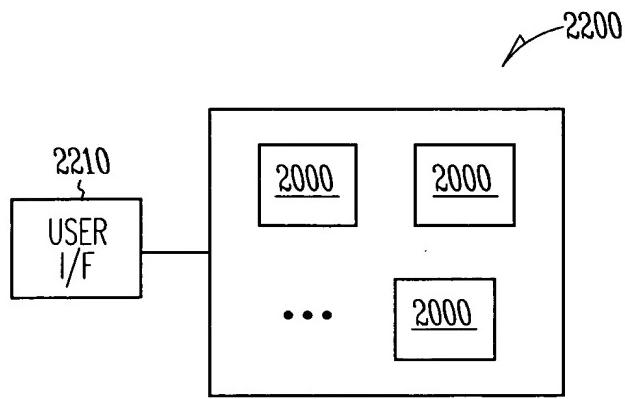


Fig. 22

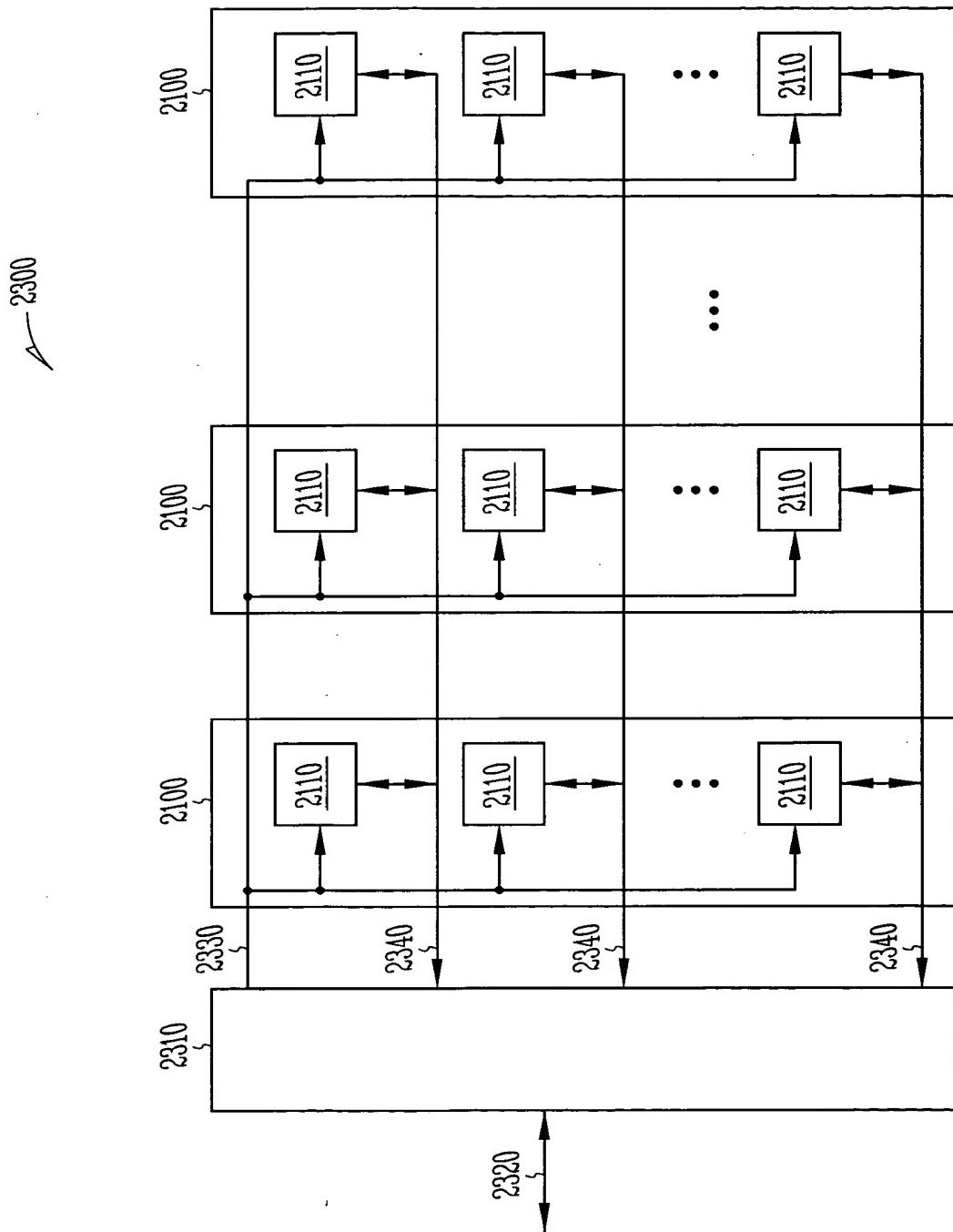


Fig. 23

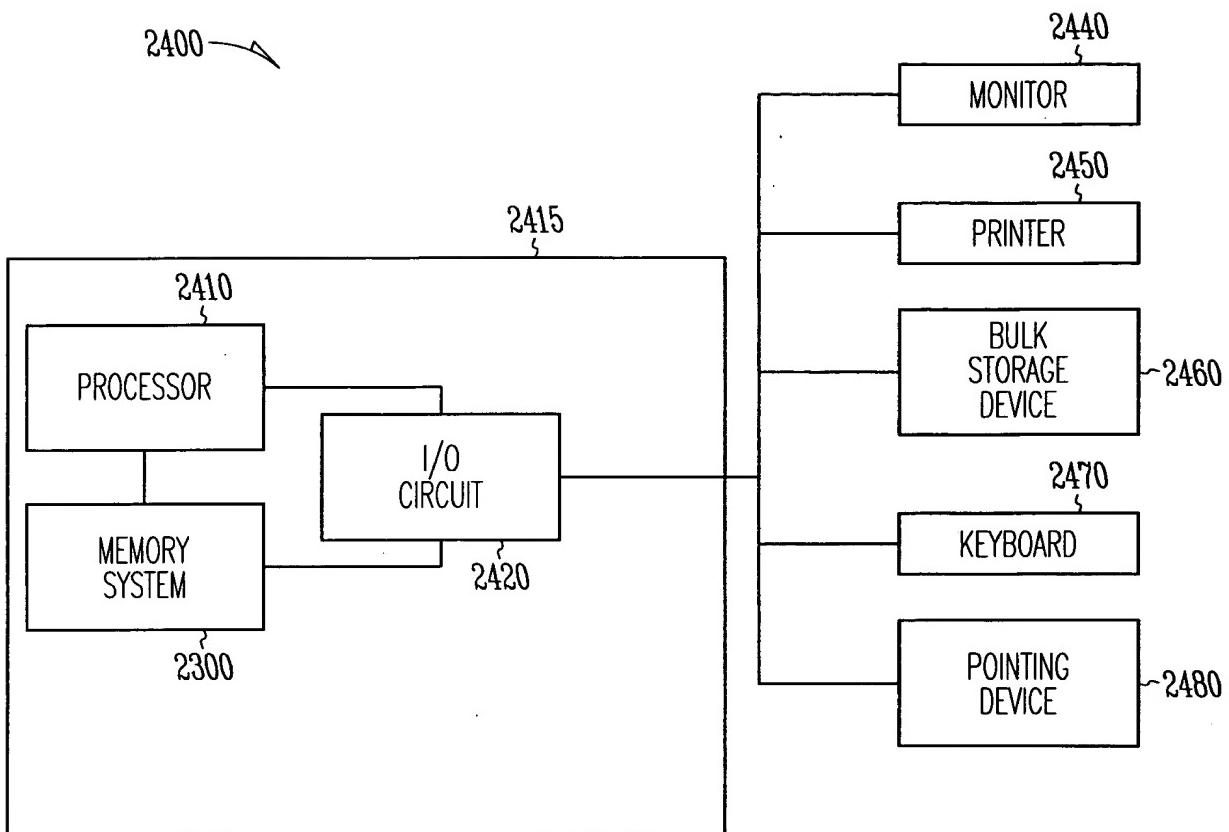


Fig. 24